

**Provisional List of the Eligible/Ineligible candidate for written test/Interview in the
Department of ELECTRONICS AND COMMUNICATION ENGINEERING for Ph.D. Admission ODD Semester 2025-26**

Sr No	Form No	Applicant Name	Department	Admission Category	Admission Sub Category	Eligible/ Ineligible	Reason for Ineligible	Entrance written Test waived off (Yes or No) if yes give reason	Remarks
1	PHD/2025/4415	KAVISHA YADAV	Electronics & Comm. Engineering- ECE	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Eligible		No	
2	PHD/2025/4419	KAUSHAL KISHORE TIWARI	Electronics & Comm. Engineering- ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Provisionally Eligible		No	1. Subjected to verification of MS Degree and its equivalence to ME/MTech; 2. PG without GATE Score; 3. Current GATE Score not valid
3	PHD/2025/4519	OM PRAKASH YADAV	Electronics & Comm. Engineering- ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Provisionally Eligible		No	1. MTech in Biomedical Engg.; 2. Mtech done with GATE score, not clear; 3. Current GATE Score not valid
4	PHD/2025/4574	SHEETAL VERMA	Electronics & Comm. Engineering- ECE	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Ineligible	Percentage in qualifying degree< 60 %		
5	PHD/2025/4604	ZEESHAN ALAM	Electronics & Comm. Engineering- ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	Valid NET score
6	PHD/2025/4646	RUPA KALA	Electronics & Comm. Engineering- ECE	Off Campus	Off Campus (Off Campus (beyond 70 km from Jaipur)	Eligible		No	
7	PHD/2025/4845	KAVISHA SINGH	Electronics & Comm. Engineering- ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Provisionally Eligible		No	NET Score validity to be ensured.
8	PHD/2025/4918	JITENDRA SINGH KUNTAL	Electronics & Comm. Engineering- ECE	Part Time	Part Time (candidate working within 70 km of Jaipur)	Eligible		No	
9	PHD/2025/4959	SANDESH SINGH SHEKHAWAT	Electronics & Comm. Engineering- ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	Valid GATE Score
10	PHD/2025/5018	SANDESH SINGH SHEKHAWAT	Electronics & Comm. Engineering- ECE	Part Time	Institute Project Staff (Working in MNIT Jaipur)	Ineligible	MNIT Project Details not provided		
11	PHD/2025/5067	BHUDEV TYAGI	Electronics & Comm. Engineering- ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Provisionally Eligible		No	1. Subjected to validity of GATE and NET Scores;
12	PHD/2025/5089	ANIKET VERMA	Electronics & Comm. Engineering- ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	1. Valid GATE Score; 2. Result Awaited

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13	PHD/2025/5194	PABITRA MAITY	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	1. Valid GATE Score; 2. Result Awaited
14	PHD/2025/5219	MOHIT RAVINDRA THAKUR	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Provisionally Eligible		No	MTech in Defence Technology
15	PHD/2025/5281	NEELAM KUMARI	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Eligible		No	
16	PHD/2025/5544	PRACHI JOSHI	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	Valid GATE Score
17	PHD/2025/5560	VIPRA BOHARA	Electronics & Comm. Engineering-ECE	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Eligible		No	
18	PHD/2025/5354	KANCHAN SENGAR	Electronics & Comm. Engineering-Embedded Systems	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Eligible		No	
19	PHD/2025/4059	MD AMIR SOHEL	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	Valid GATE Score
20	PHD/2025/4088	NARESH NOGIYA	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	Valid GATE Score
21	PHD/2025/4177	ISHA PANDEY	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Eligible		No	
22	PHD/2025/4582	ASHISH CHAUHAN	Electronics & Comm. Engineering-VLSI Design	Part Time	Part-Time (Executive/Professional)	Provisionally Eligible		Yes	1. Executive.Professional Category; 2. Overlapping in the service period from 2018-2020
23	PHD/2025/4596	HARSHITA CHORDIYA	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Eligible		No	Mtech done with valid GATE Score

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24	PHD/2025/4709	ASHISH DHAKAR	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Ineligible	With BTech Degree having < 75% marks		
25	PHD/2025/4849	SANDEEP CHANGERIWAL	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	1. Valid GATE Score; 2. Mtech in Biomedical Engineering
26	PHD/2025/5044	DESH DEEPAK LAWANIA	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Institute Assistantship (with valid GATE/UGC-NET/CSIR-NET/National Level Exam) or (Masters degree with valid GATE Score)	Eligible		Yes	Valid GATE Score
27	PHD/2025/5123	SANJAY KUMAR BAGARIA	Electronics & Comm. Engineering-VLSI Design	Part Time	Institute Staff (Working in MNIT Jaipur)	Provisionally Eligible		No	Subjected to verification of eligibility through MTech Degree.
28	PHD/2025/5179	RAKESH JAIN	Electronics & Comm. Engineering-VLSI Design	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Eligible		No	
29	PHD/2025/5374	ALKA RANI	Electronics & Comm. Engineering-VLSI Design	Part Time	Part Time (candidate working within 70 km of Jaipur)	Eligible		No	
30	PHD/2025/4234	AASHISH KUMAR SHARMA	Electronics & Comm. Engineering-Wireless and Optical Communication	Full Time	Full Time with Financial Assistantship (without NET/GATE)	Eligible		No	