Organized by
Electronics & ICT Academy

MNIT Jaipur & LNMIIT Jaipur
http://www.mnit.ac.in/eict

Online Programme on
Hardware–Software Co- Design Approaches for VLSI-Based Embedded Systems

30th Nov – 11th Dec 2026

Faculty Development Programme
Electronics & ICT Academy under aegis of

Ministry of Electronics and Information Technology
Government of India

meity.gov.in/content/schemes-projects

Experts/Speakers-

Prof. Subir Kumar Roy, IIIT Bangalore
Prof. Bishnu K Das – IIT Roorkee
Prof. Jawar Singh – IIT Patna
Prof. Vineet Sahula – MNIT Jaipur
Prof. Manish Goswami – IIIT Allahabad
Prof. Venkatesh Subramanian – IIIT Allahabad
Prof. Balwinder Raj – NIT Jalandhar
Dr. Amit Joshi – MNIT Jaipur
Mr. Nirupam S.D. Spectra AI PTE Ltd. Singapore
Mr. Raghav Pachaury – CoreEL Pvt. Ltd
Mr. Ankur Sangal – HCL Tech (Sankalp)

EICT Academy MNIT Jaipur

Chairman, EICT Academy & Director MNIT Jaipur **Prof. Narayana Prasad Padhy**
Chief Investigator: **Prof. Vineet Sahula**
Coordinator: **Dr. Satyasai Jagannath Nanda**
Co- Chief Investigators: **Prof. Lava Bhargava, Prof. Pilli Emmanuel Shubhakar, Dr. Ravi Kumar Maddila**

Objective (Electronics & ICT Academy-Phase II)

1)To conduct specialized FDPs for faculty/mentor training in line with the vision of MeitY in high-priority areas supporting the "Make in India" and the "Digital India" programs.

2) To promote synergy and collaboration with industry, academia, universities and other institutions of learning

3) To support the National Policy on Electronics 2019 (NPE 2019), which envisions positioning India as a global hub for ESDM sector, including MeitY Schemes/policies such as Programme for Semiconductors and Display Fab Ecosystem; India AI; National Programme on AI, Production Linked Incentive Scheme for IT Hardware & Large-Scale Electronics Manufacturing; EMC; SPECS; Chips to System (C2S); etc.

4) To promote standardization of FDPs through Joint Faculty Development Programmes.

A foundational 40 Hours Training Programme in online mode is being organized for faculty and doctoral students of engineering and technological institutions. It is also open to working professionals from industry/organizations. The main theme of training program will be oriented around exploring the state of the art methods for Hardware–Software Co- Design Approaches for VLSI-Based Embedded Systems.

The programme will be run daily (Mon to Fri) during 1:00 PM to 5:00 PM

Programme Modules:

Module 1: VLSI Design Foundations and Emerging Semiconductor Technologies.
Module 2: Hardware–Software Co-Design and System-Level Design Methodologies.
Module 3: Embedded Systems, SoC Design and RISC-V Architectures.
Module 4: FPGA-Based Prototyping, Reconfigurable Computing and Hardware Acceleration.
Module 5: AI/ML Accelerators, Edge Computing and Intelligent Embedded Systems.
Module 6: VLSI Design Verification, Testing and Design-to-Silicon Flow.
Module 7: Emerging Trends and Future Directions in Hardware–Software Co-Designed Systems.

Programme Coordinator		
Dr. Sandeep Saini	fdp.academy@mnit.ac.in	9983180460 (M)

Registration: Registration is open to faculty, working professionals, industry persons, doctoral, postgraduate, and graduate students from India and the rest of the world. Participants will be admitted on a first-come, first-served basis.

Registration Fee			
Mode of programme	India/SAARC/Africa (18% GST extra)		Rest of the world
	Academia (faculty/Students)	Others	
Online	Rs. 750/-	Rs. 1500/-	US $ 60/-

- Fee once paid will not be refunded back; however, can be adjusted for a future programme.
- The fee covers online participation in the programme, tutorial, notes and examination, certification charges etc.
- The registration amount may be paid through online mode-NEFT/UPI/Cards/SWIFT, provided at the registration portal.
- A detailed schedule will be shared after receiving the registration form.

Register online at–http://online.mnit.ac.in/eict/

→For queries,**contact us at** fdp.academy@mnit.ac.in

EICT Academy MNIT Jaipur

MNIT Jaipur one of the oldest NITs, the institute has a rich heritage of sixty years producing world class engineers, managers, architects and scientists. Ranked 42nd nationally in the NIRF ranking-2025 (Engineering), the institute offers learning opportunities for undergraduate, postgraduate students, and researchers in various domains. Having a lush green campus of over 317 acres within the heart of the pink city, close to Jaipur International Airport, the campus offers a safe and lively environment. A world class teaching infrastructure, state-of-art laboratories welcome you at the campus. The institute has a vision to impart education of international standards and conduct research at the cutting edge of technology.